ECE 340 Lecture 23 : Qualitative Current Flow in a P-N Junction

Class Outline:

•Qualitative Current Flow in a P-N Junction

Things you should know when you leave ...

Key Questions

- What happens to a p-n junction under a forward bias?
- What happens to a p-n junction under a reverse bias?
- What physical processes are in play?
- What can I safely ignore when I apply a bias?



We understand the electrostatics:



We have a tentative game plan for solving p-n junction problems... Plan for finding the charge density, electric field, and potential:

- 1. Find the **built-in potential**, V_0 .
- 2. Use the depletion approximation to find the charge density.
 - Gives an easy solution to the Poisson equation.
 - Depletion layer widths are still unknown.
- 3. Integrate the charge density to find the electric field.
 - Be sure to use the appropriate boundary conditions $E(-x_p) = E(x_n) = 0$.
- 4. Integrate the **electric field** to find the **potential**.
 - Boundary conditions are $V(-x_p) = 0$ and $V(x_n) = V_0$.
- 5. Solve for the **depletion layer widths** $(-x_p \text{ and } x_n)$.

• Use the fact that for E(x) to be continuous at x = 0, $N_A x_p = N_D x_n$. *M.J. Gilbert ECE 340 - Lecture 23* 10/14/11



Now apply a bias. What do we know?



Things we know or will assume:

•We are assuming that the contacts to the p-n junction are ohmic.

•We are assuming that there is no voltage drop in the bulk of the p and n regions (low level injection).

•We assume that all of the voltage is dropped across the space charge region.

•We assume that $V_A < V_0$ otherwise we cannot assume low level injection.

So what will happen to the **potential**...

Forward Bias ($V = V_F$):



 $(V_0 - V_f)$

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•The potential barrier is lowered for a forward bias.

Reverse Blus
$$(v - v_R)$$
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reverse bias.

•The

potential

barrier is

raised for a

What will happen to the electric field and space charge region?



When we apply a bias we are changing the electric field which will change the space charge region as we still need the proper number of positive and negative charges.

•Apply a **positive bias** and we will **decrease the electric field** because the bias potential will oppose it. We also expect a **smaller space charge region** because of the smaller field with reduced uncompensated charge.

•Apply a **negative charge** and we will **increase the electric field** as the bias potential is now in the same direction as the field. We expect a **larger space charge region** because we now have more uncompensated charge to balance.

•We can already anticipate the change in the energy bands as a **forward bias** will bring the **bands closer** to one another.

•Naturally, if we then apply a **reverse bias** the energy bands, the bands will **separate farther apart**.

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 The bias also separates the Fermi levels with E_{FN} > E_{FP} by V_f for forward bias and E_{FN} < E_{FP} by V_r for reverse bias.

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Let's summarize the electrostatic changes with applied bias...

- How should the **diffusion current** behave?
- •The diffusion current is majority carriers on the n-side surmounting the barrier and crossing over to the p-side.
- •Some high energy electrons can surmount the barrier at equilibrium.
- •Under forward bias, both electrons and holes begin to diffuse creating a significant current.
- •Under reverse bias, the barrier to diffusion is raised and very few carriers can diffuse from one region to another.
- •Diffusion current is usually negligible for reverse bias.

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Taking a closer look at the **forward and reverse bias carrier concentrations**...

Physics of Semiconductor Devices, S.M. Sze, Wiley-Interscience

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Where there is diffusion, there is also drift current...

•The drift current is relatively insensitive to the height of the potential barrier.

•The drift current is not limited by how fast carriers are swept down the barrier but instead it is **limited by how often** they are swept down the barrier.

•Minority carriers wander too close to the space charge region and are swept across.

•This leads to a drift current.

•But there are not many carriers available to be swept across so this leads to a small current.

•Every minority carrier that participates will be swept across regardless of the size of the barrier.

•Minority carriers are generated by thermal excitation of EHPs.

•EHPs generated within L_p or L_N of the SCR will participate.

 Referred to as generation current.

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Summarizing the total current in the p-n junction...

Equilibrium: •No current flows

Reverse Bias:

•Both drift and diffusion currents are very small.

•Only current that flows is from the generation process..

•This current is bias independent.

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Take a closer look at the **forward bias** regime...

Forward bias increases the probability of diffusion across the junction exponentially.

 $I = I_0(e^{qV/kT} - 1)$

Total current is the diffusion current minus the absolute value of the generation current.

At V = 0, the generation and diffusion currents cancel.

End result is a rectifying type of behavior seen in MS contacts.

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Let's try to visualize what is going on...

What happens to the holes??

•As the hole reaches the end of the semiconductor, it recombines with an electron which must be supplied by the external circuit.

•As one hole disappears, another hole must appear at the entrance of the circuit to conserve charge neutrality.

•So, we have the generation of an electron-hole pair when an electron leaves the semiconductor sample.

•The hole flows in while the electron flows out.

Now look at the big picture of a p-n junction under bias...

When electrons are swept across the junction they are replaced by an electron generated from an R-G center. Similar for holes.
Excess minority carriers set up a local field pushing carriers to the contacts.

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Let's solve a simple problem...

The electrostatic potential in the depletion region of a p-n junction under equilibrium conditions is determined to be:

$$V(x) = \frac{V_{bi}}{2} \left[1 + \sin\left(\frac{\pi x}{W}\right) \right] \qquad -\frac{W}{2} \le x \ge \frac{W}{2}$$

(a) Establish a relationship for and sketch the electric field in the depletion region.

(b) Establish a relationship for the charge density and sketch it.

(c) Invoke the depletion approximation, determine and sketch $N_{\rm D}$ – $N_{\rm A}$ in the depletion region.

Let's solve a hard problem...

A P-I-N diode is a three-region device with a middle region that is intrinsic and relatively narrow. Assuming that the p and n regions are uniformly doped and ND - $N_A = 0$ in the intrinsic region:

(a) Sketch the expected charge density, electric field, electrostatic potential and band diagram.

(b) What is the built-in voltage drop between the p and n regions? Justify it.

(c) Establish a quantitative relationship for the charge density, electric field, potential and the depletion widths.

